

Orbit CMM to Host Interface Specification



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1. Diary of changes

- Issue 1.0.....February 2002
- Issue 1.1.....March 2002
- cs (checksum) reference changed to cc
- Issue 1.2.....6th Sept 2002
- Modification to disclaimer.
- Issue 2.0.....22nd Nov 2002
- [Appendix 4: Additional commands for units fitted with the Scorpion Coin Acceptor](#) added.
 - Battery supply details added to [Appendix 5: Hardware Detailed Requirements.](#)
 - Changed manual title from '8 Coin' to 'Orbit'.
 - Added details to [Appendix 5: Hardware Detailed Requirements.](#)

2. Message Format

This document defines the software requirements to implement the protocol between the Coin Management Module and the controlling application, e.g. Host Main Control Board. It describes the procedure for information exchange, the frame characteristics, and the fields within the message packets. The CMM version with a C120P coin acceptor supplied by Money Controls is described in this document.

This document also describes the initialisation process for the CMM.

3. Message and Frame Characteristics

Transmission of data between nodes uses an asynchronous serial bus. Each message frame consists of several characters.

3.1 Character Format

The character format is shown below and consists of 1 start bit, 8 character bits (sent lowest bit first), and 1 stop bit. No parity bits are required.

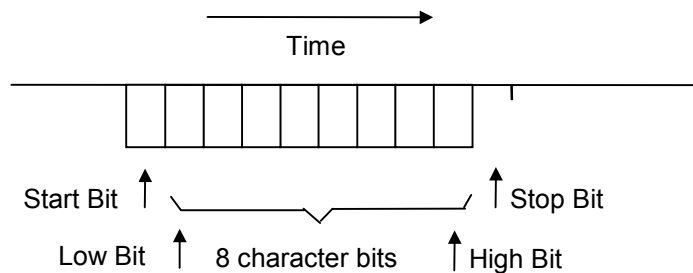


Figure 1: Character Format

Characters are transmitted at a Baud Rate of 4800.

3.2 Message Frame Fields

Each frame consists of 5 fields contained within 8 bytes and is shown in the diagram below.

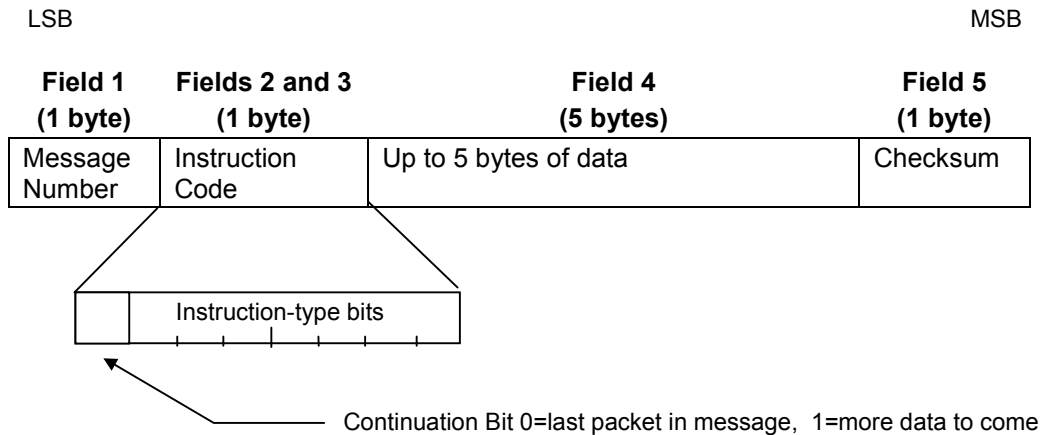


Figure 2: Message Frame Fields

The first byte is used to identify the message packet, so that in the event of a message being re-transmitted, it is not interpreted as a second occurrence of the initial message. Each time a message transfer is completed, this message number is incremented, and after 255 messages it rolls around to message 0.

The second byte is used to describe the instruction code for this message packet. The 2 fields in the instruction code are 1 (continuation bit) and 7 (instruction type bits).

The continuation bit is set if the data contained in the message packet is incomplete, and more data is to come in further messages. In the last message of a string of continued messages, the continuation bit will be reset to zero.

The instruction type bits define which instruction is being sent.

The third to seventh bytes contain the fourth field, which is up to 5 bytes of data relevant to the instruction. If more than 5 bytes are required then the continuation bit in the packet is set. Any unused bytes must be padded out.

The eighth byte contains the message packet checksum. This is evaluated by adding the values of all other bytes together, taking the two's compliment of this total and adding 1. Hence the sum of all of the bytes in the message (including the checksum) will be zero, ignoring any carry that occurs.

For transmission of a message to the CMM, the 'CMM_Busy' line from the CMM must be checked to ensure that the CMM is in a state where it can receive a message.

4. Message Handling Procedures

4.1 Pre-transmission Requirements

If the 'CMM_Busy' line is inactive, the following procedure should be followed:

- Raise the wake-up line to the CMM
- wait a minimum of 5ms
- send the message
- lower the wake-up line

This provides a frame for the message.

If the 'CMM_Busy' line is active, then the CMM is busy, and a message cannot be sent to the CMM at this time.

Note: The 'CMM_Busy' line is active HIGH. For transmission of messages from the CMM, the host is assumed to always be in a state to receive messages from the CMM and therefore no busy checking is required. The CMM will raise the 'CMM_Busy' and wait 20 ms before sending, if a message arrives from the host equipment in this time the CMM will back off for 50ms and handle the incoming message.

If the host equipment wishes to prevent messages being sent by the CMM, it can put the CMM in a mode where outgoing messages are inhibited. (See Appendix 1, section a) Operating Mode)

The diagram below shows the connections between the CMM and the host equipment.

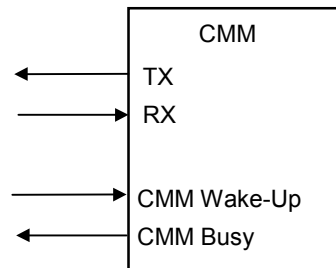


Figure 3: CMM to Host Connections

4.2 Message Protocol

When a message is to be sent, the transmitting node will build the message frame, pass the message into a transmission buffer, and start a 'no reply' timer.

On receipt of a message packet the receiving node will check for framing errors. If any errors are found a 2-byte 'NACK' message is immediately returned to the transmitting node, with the reason code (listed below) set to indicate a hardware transmission error.

Providing there is no framing error, then the checksum is evaluated, and if incorrect, will cause a 'NACK' message to be sent with the reason code set to communication error. If no such errors are found, then the instruction code is checked against a list of valid instructions relevant to that node.

An invalid instruction will prompt the receiving node to return a 'NACK' message to the originating node, with the reason code set to unknown instruction, and an echo of that instruction. The transmitting node can then verify that the instruction sent was correct. A valid instruction will result in an 'acknowledge' message being sent.

The diagram below shows the configuration of the 2 byte return message to the originating node.

	1 st Byte	2 nd Byte
Acknowledge	1 1 0 0 X X X X	X X X X X X X X
	X = don't care	
Not Acknowledge ("NACK")	0 0 1 1 r r r r	i i i i i i i i
	Reason Code	Instruction Echo

Table 1: Transmission Error Codes

Reason Code	1st byte	Reason for Failure
00	30	Unknown Message
01	31	Comms Error (checksum fail)
02	32	Hardware Error

The message number byte can be used to determine if repeat messages are being received. When the transmitting node initiates the sending of a message, the message number is incremented. The receiving node can then compare the message number of the last message and the new incoming message, and similarly the message type, and decide whether the message is a repeat. (A repeat message may be caused by the transmitting node not receiving the acknowledge correctly.) The incoming message can then be either ignored if it is a repeat message, or actioned. (The message must still be acknowledged as valid, in the normal manner)

Note: The receiving node can only transfer the 'incoming message' number into the 'last message' number store when the incoming message has passed all tests and the instruction received actioned.

If the transmitting node does not receive a valid reply (either 'NACK' or 'acknowledge') before the 'no reply' timer expires then the transmitting node will attempt to re-send the message, up to a maximum of MAX. TRANSMISSION RETRY (recommended value 3) times. After the maximum number of re-sends, the node will stop sending that message and increment an interface failure counter. This failure counter can then be interrogated via diagnostics or self test routines.

If a message fails to be transmitted, it will remain on the message queue, and further attempts to send the message will be made when the transmitting node is reactivated. After the node has been reset, all un-transmitted messages are removed from the queue, except for fault type messages.

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5.6 Refund Amount. (Hex code 06)

This message is sent by the CMM after a 'final cost of call' message. The data field contains the monetary value of the refund (4 bytes) to be given.

Packet from CMM:	nn	06	rv	rv	rv	rv	00	cc

			LSB			MSB		
			Refund Value (Hex)					

5.7 CMM Parameters. (Hex code 07)

This command message is sent to the CMM. This message will contain at least 2 packets, the first packet containing a 1-byte identifier in the first data byte, indicating the data type being sent. The second (and any following packets) will contain the relevant data, in the strict order described in following parameter descriptions.

1st Packet to CMM:	nn	87	pp	00	00	00	00	cc
			Parameter Identity (see below)					
nth Packet to CMM:	nn	87	dd	dd	dd	dd	dd	cc

			Parameter Data					
				:				
				:				
Last packet to CMM:	nn	07	dd	dd	dd	dd	dd	cc

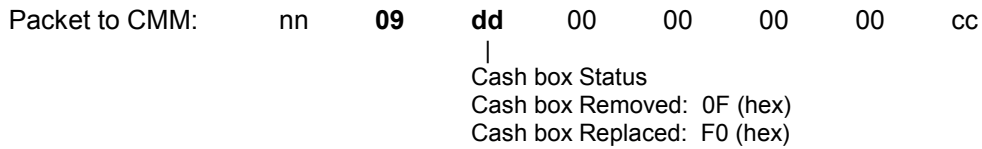
			Parameter Data					

After this message has been received, the CMM must reply with a 'CMM parameter update status' message, which will indicate success or failure of the update, and the nature of any fault that may occur.

Parameters that can be updated are listed in Appendix 3.

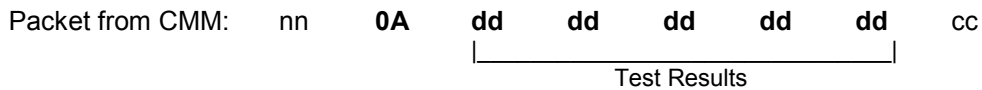
5.9 Cash box Status. (Hex code 09)

This event message is sent to the CMM when the Cash Box State is changed. The first data byte indicates the new state, and is set to 0F(hex) for cash box removed, and F0(hex) for cash box replaced. On Cash box removal, a cash box data reply message is sent by the CMM.



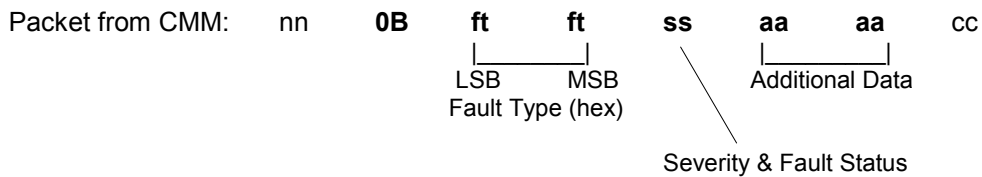
5.10 Test Result. (Hex code 0A)

This message is sent by the CMM after completion of a diagnostic test. The data bytes contain the results of the test. (See appendix 2.)



5.11 Fault Message. (Hex code 0B)

This event message is sent by the CMM when a fault is detected by the CMM. The data space (bytes 3 to 7 in the message packet) in the fault message provides information about the fault, in the following format.



The first and second data byte describes the fault type. Each group is attributed an offset, these being 1100 and 1400 respectively, to which the fault code is added. The third data byte has 2 functions, indicating the fault severity, and whether a fault is being raised or cleared. The high nibble describes the fault severity and the low nibble describes the fault status.

Table 3: Fault Messages

Data Byte		Meaning
Severity	0x	Warning Fault, coin call service is unaffected.
	1x	Fatal Fault, coin call service is disabled.
Status	x0	Incident, no raise or clear.
	x1	Raising a fault.
	x2	Clearing a fault.

The fourth and fifth byte may contain fault-specific additional data.

The fault conditions and fault codes (decimal, with hex value in brackets) sent by the CMM are as follows:

Table 4: Fatal Store Faults

	Store
Motor Current Fail	1400 (578 hex)
Cashing Errors Exceeded	1401 (579 hex)
Store Blocked	1402 (57A hex)
Store Positioning Failed	1403 (57B hex)
Store Sensor Fail	1404 (57C hex)
Refund Fail	1405 (57D hex)

Table 5: Warning Store Faults

	Store
Refund NOT Cash Error	1410 (582 hex)
Cash NOT Refund Error	1411 (583 hex)
Coin Stuck in Pocket	1412 (584 hex)

Table 6: Fatal Faults

Flight Deck Open/Closed	1120 (460 hex)
Escrow Entry Blocked	1121 (461 hex)
Coin Exit Sensor Blocked	1122 (462 hex)
CMM Memory Fail	1123 (463 hex)

Table 7: Warning Faults

CMM/Acceptor Interface Fail	1130 (46A hex)
Not used	1132 (46C hex)
Coin Insertion Limit Exceeded	1133 (46D hex)
Max. Coin Rejects Exceeded	1134 (46E hex)
Coin Reject/Coin Accept ratio Exceeded	1135 (46F hex)

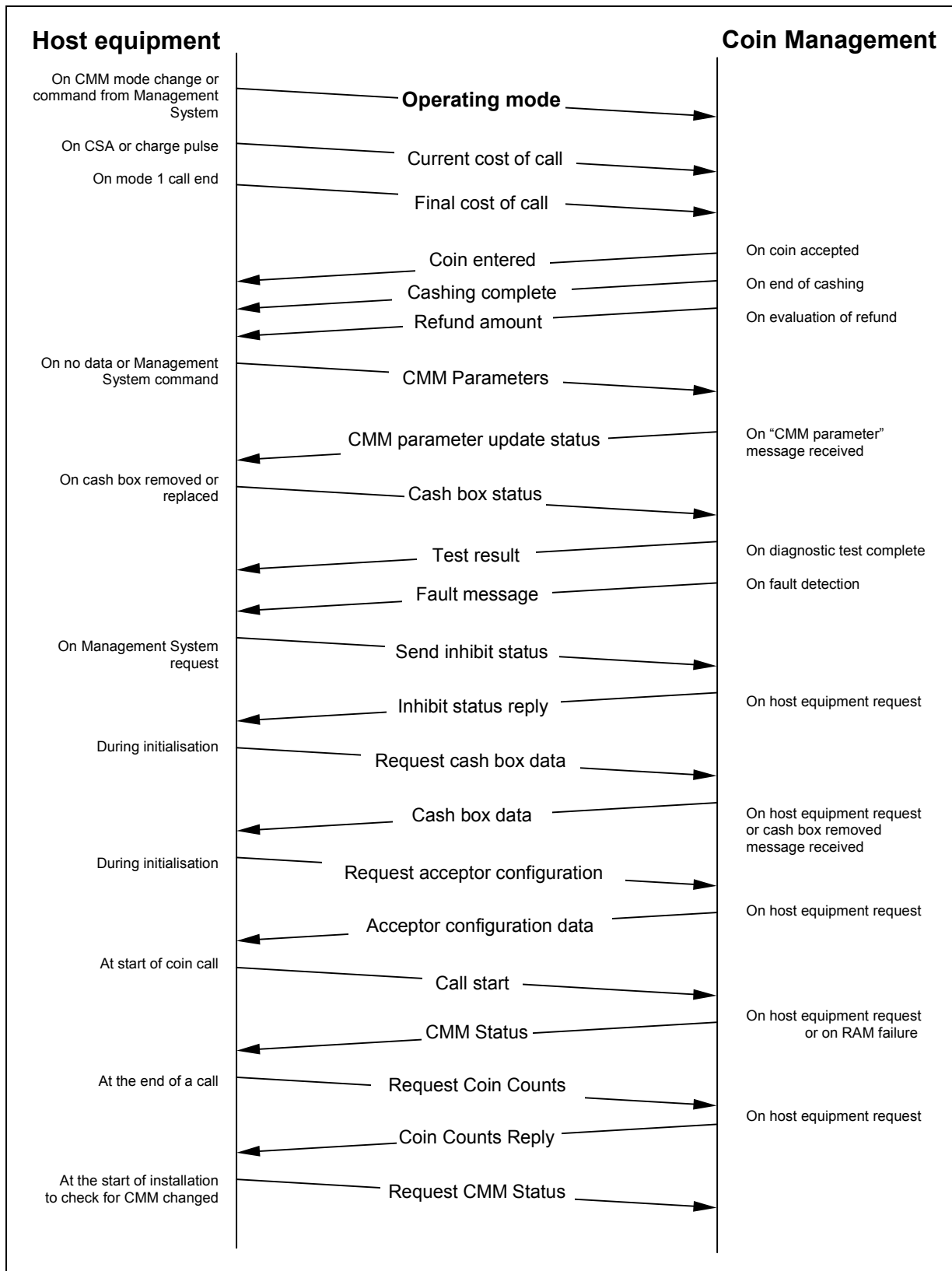


Figure 4: Coin Management - Host Equipment data interchange

6. Appendix 1: Installation & Initialisation Routine

When the host equipment powers-up, the internal modules must be able to communicate, and follow a defined procedure to determine if any or all of the modules have been replaced. If it is the case, that new modules have been situated in the host equipment, then initialisation of these new modules is needed.

This appendix describes this procedure, and the required functionality of both the CMM and the host equipment. Figure 2 shows the message interchange for this procedure.

The procedure can be divided up into 2 phases, these being:

1. Installation
2. Initialisation

On every power-up condition, the initialisation procedure will begin at phase 1.

6.1 Phase 1: Installation

After power-up, the host equipment will begin the procedure by evaluating whether it needs to download new data from the Management System. If this is not required, then the procedure moves directly to phase 2, otherwise the host equipment should request Acceptor data from the Management System.

The host equipment receives all data up to the coin signature download service, and then decides from previous data if the coin signature service is required.

If the coin signature service is required, the host equipment must contact the CMM in order to obtain the acceptor configuration data, using the "request acceptor configuration data" message. The CMM having done a first stage internal initialisation, will interrogate the acceptor and send the information back to the host equipment using the 'acceptor configuration data' message. This data is then used in creating the correct coin signature database to be returned to the host equipment.

If the host equipment does not receive a reply from the CMM, then it will assume that no CMM is present, and a fault report is required to indicate that access to the CMM has failed.

The host equipment now sends a 'Request CMM Status' message to the CMM, containing the host identity, and the acceptor programming mode. The CMM returns a 'CMM Status' message, which describes to the host equipment which data is required, if any. The host equipment sends the data to the CMM using the 'CMM Parameter' message.

Once the download has been received, the procedure moves on to phase 2, but note that the host identities will match, and the host will be in full service, and able to accept coins.

6.2 Phase 2: Initialisation.

In order to initialise the CMM, the host equipment must ensure that the CMM currently installed has not been exchanged since the last power-up. The host equipment sends the 'Call Start' message to the CMM, which contains the host identity and the acceptor program mode for the host. The CMM will then compare the received host identity, with the host identity already held by the CMM. The CMM also ensures on power-up, that the acceptor has not been changed.

If the acceptor identities are different, and/or the acceptor has changed with the CMM unable to re-program it, then this CMM requires re-initialisation. A 'CMM status' message is sent indicating as such, followed immediately by a 'Acceptor Configuration Data' message. The host equipment now has the required data to obtain a coin set download, and if the acceptor config. data is different to that which it has already stored, then at the end of the call the host equipment will communicate with the Management System, receive the appropriate download, and transfer the data to the CMM (and hence the acceptor). When coin set download is complete, the host equipment must also convey the new host identity, and the latest cash box totals, to the CMM using the 'CMM parameter' message. Once this is done, initialisation is complete.

If the host identities and the acceptor configuration data match, then this CMM has not been exchanged, and the 'CMM status' message is sent indicating such. If the acceptor configuration data is different but the CMM was able to re-program the acceptor, then a 'acceptor configuration data' message will be sent following the 'CMM status' message. (The status will be set CMM unchanged, Acceptor changed, and does not require a download). This will allow the host equipment to update it's own configuration data.

The host is now ready to accept coins. (Note: If the scenario is such that the host equipment has been changed, but not the CMM, then at the end of this call sequence, the cash box totals in the host equipment will be updated as normal, in the 'cashing complete' message.)

These 2 phases of the installation/initialisation procedure can be seen in figure 2 which shows the typical message interchange for the case of a host being installed with a 'new' host equipment and a 'new' CMM.

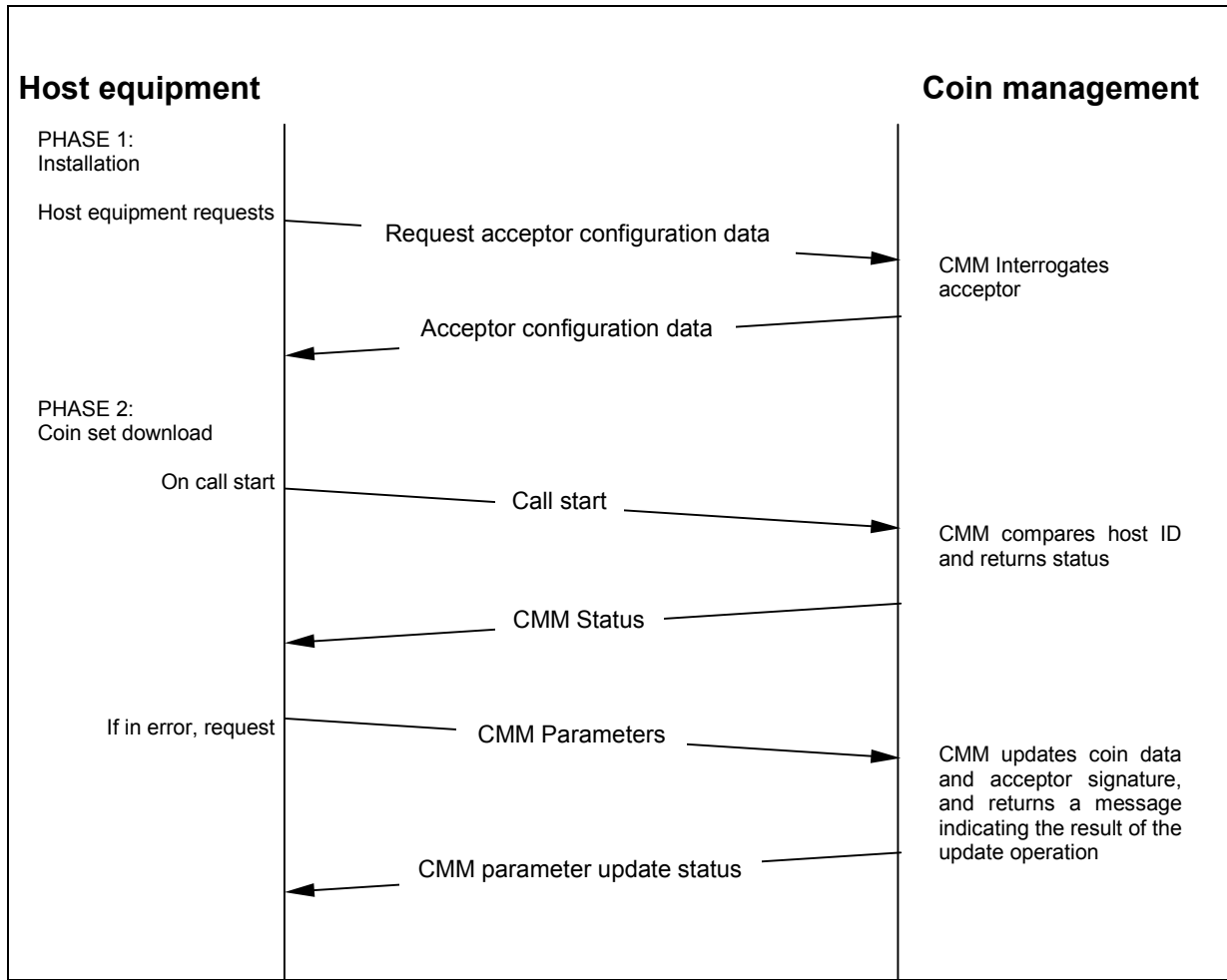


Figure 5: Host Initialisation Procedure

7. Appendix 2: Diagnostic Commands

The CMM diagnostic mode is entered using the operating mode command (instruction 01, mode change 03), and followed with a test number. Any one from a suite of tests can be performed by sending the 'enter diagnostic mode' operating message with the test code byte (2nd data byte) set to one of those listed below.

7.1 Test Call. (Test Code 01)

This test puts the CMM into test mode, and behaves as if a standard call is in progress, except that any coins cashed will not be added to the coin totals. No data is expected from the CMM in response.

Packet to CMM: nn **01** **03** **01** 00 00 00 cc

7.2 Self-Test. (Test Code 02)

This test performs a series of tests without the need for any input from the test engineer, and will check the CMM/Acceptor interface link, all sensors, and the motor. A test result message will be returned to the host equipment, with the outcome of the test described by the data bytes in the following format. The first block will indicate the average time in milliseconds for the motor to turn a store through one pocket, (i.e. 1/9th of a revolution). The value is only valid if the motor test has passed as indicated in the second block, and is contained in 2 bytes, with the lowest significant byte first.

Packet to CMM: nn **01** **03** **02** 00 00 00 cc

nn **8A** **ms** **ms** **ms** **ms** 00 cc

Average Time for 1 pocket to rotate (ms).

nn **0A** **tr** **tr** **tr** **tr** **tr** cc

Spare

Acceptor Interface Test Result

Motor Test Result

Sensor Test Result

Acceptor Self Test Result

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Table 10: Acceptor Self Test result byte for Money Controls C120P

Data	Meaning
0	OK (No Fault Detected)
1	EEPROM checksum corrupted
2	Fault on Inductive Coils
3	Fault on Credit Sensor

Table 11: Sensor test result byte:

0000	0000	Test Passed
xxxx	xx01	Test inconclusive, Store Jammed.
xxxx	xx10	Store sensor permanently OFF
xxxx	xx11	Store sensor permanently ON
xxxx	01xx	Not used.
xxxx	10xx	Reverse store sensor permanently OFF
xxxx	11xx	Reverse store sensor permanently ON
xx10	xxxx	Exit sensor permanently OFF
xx11	xxxx	Exit sensor permanently ON
10xx	xxxx	Gate sensor permanently OFF
11xx	xxxx	Gate sensor permanently ON

Table 12: Motor test result byte:

0000	0000	Test Passed
xxxx	0001	Motor stalled
xxxx	0010	Motor Disconnected
xxxx	0011	Blockage in store
xxxx	0100	Store Position Fail
xxxx	0101	Store Not Moving
xxxx	0110	Store Rotation Speed varies by $\pm 12.5\%$
xxxx	0111	Unable To Perform Test
xxxx	1xxx	Not used
0001	xxxx	Not used
0010	xxxx	Not used
0011	xxxx	Not used
0100	xxxx	Not used
0101	xxxx	Not used
0110	xxxx	Not used
0111	xxxx	Not used
1xxx	xxxx	Not used

(x = does not matter.)

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Table 13: Acceptor Interface test result byte:

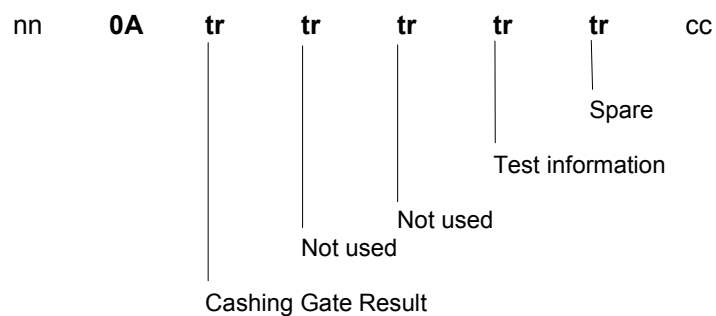
Data	Meaning
08	Transmit Message Fail
40	Receive Message Fail
80	No Response Time out

For a complete list of all possible acceptor fault codes see acceptor specification.

7.3 Manual Test (Test Code 03)

Packet to CMM: nn **01** **03** **03** 00 00 00 cc

This test checks the operation of the cashing gates. This test requires that the test engineer fill the CMM with 8 coins. On each coin entered, a coin accepted message is sent to the host equipment in the normal manner. When the escrow is full, all coins are then refunded and cashed alternately (refund first), and a test result message sent. The format for the test result will be as described below.



Cashing Gate Test Results:

High Nibble	Low Nibble
Number of Refund-not-Cash failures	Number of Cash-not-Refund failures

Test Information:

The low nibble indicates the number of coins fed into the CMM. If the high nibble is set to 1111, then the test has been aborted due to a time out.

7.4 Status Test (Test Code 04)

Packet to CMM: nn 01 03 04 00 00 00 cc

This test polls all the fault flags within the CMM, and returns a test result message indicating each flag's state, in the following format.

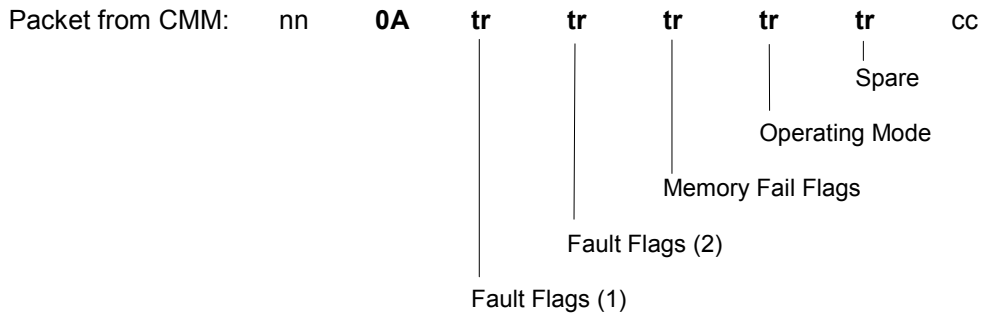


Table 14: Fault Flags (1):

0000	0000	No Faults
1xxx	xxxx	Store Disabled
x1xx	xxxx	Not used
xx1x	xxxx	CMM/MCB Interface Fail
xxx1	xxxx	Coin exit Sensor Failed
xxxx	1xxx	CMM Cashing Off
xxxx	x1xx	CMM/Acceptor Interface Fail
xxxx	xx1x	CMM Full
xxxx	xxx1	Coin Entered

Table 15: Fault Flags (2):

0000	0000	No Faults
1xxx	xxxx	Coin Store Moving
x1xx	xxxx	CMM/MCB Interface Operating
xx1x	xxxx	Updating CMM Parameters
xxx1	xxxx	Inhibit Coins
xxxx	1xxx	Cash box Out

Table 16: Memory Fail Flags:

0000	0000	Memory OK
xxxx	xxx1	Coin Signature Memory Fail
xxxx	xx1x	Coin Data Memory Fail
xxxx	x1xx	Cash box Memory Fail
xxxx	1xxx	Host Identity Memory Fail

7.7 Count Query - inserted, rejected & cancelled coins (Test Code 50)

Packet to CMM: nn **01** **03** **50** 00 00 00 cc

This is used to request the number of inserted coins in each category since the last time the totals were reset. Data for two coins is contained in one packet; the number of packets depends on the number of coin types allowed in the acceptor.

1st Packet:	nn	8A	dd	dd	00	dd	dd	cc
			LSB	MSB		LSB	MSB	
			Coin 1 Count			Coin 2 Count		
					:			
					:			
(n/2)th:	nn	8A	dd	dd	00	dd	dd	cc
			LSB	MSB		LSB	MSB	
			Coin n-1 Count			Coin n Count		
1+(n/2)th:	nn	0A	dd	dd	00	dd	dd	cc
			LSB	MSB		LSB	MSB	
			Rejected Count			Cancelled Count		

7.8 Reset Coin Totals (Test Code 51)

Packet to CMM: nn **01** **03** **51** 00 00 00 cc

This is used to reset the inserted, rejected & cancelled coin totals. No data is expected from the CMM in response.

7.9 Host ID Query (Test Code 52)

Packet to CMM: nn **01** **03** **52** 00 00 00 cc

This is used to request the host ID currently stored in the CMM. Note: The host ID is in packed BCD, each unused nibble filled with Hex E (Decimal 14).

1st Packet:	nn	8A	id	id	id	id	id	cc
			MSB	Host ID				
2nd Packet:	nn	0A	id	00	00	00	00	cc
			Host ID (LSB)					

7.10 Reset Host ID (Test Code 53)

Packet to CMM: nn **01** **03** **53** 00 00 00 cc

This is use to set the Host ID in the CMM to blanks. No data is expected from the CMM in response.

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7.11 Hard Reset (Test Code 54)

Packet to CMM: nn **01** **03** **54** 00 00 00 cc

This is used to cause the RAM in the CMM to be cleared on the next power-up cycle. No data is expected from the CMM in response.

7.12 EEL Dump (Test Code 55)

Packet to CMM: nn **01** **03** **55** 00 00 00 cc

This is used to download the EEL stored in the CMM. The EEL records are 6 bytes long and are transmitted from the CMM in a continuous data stream within a sequence of messages.

1st Packet:	nn	8A	d1	d1	d1	d1	d1	cc

			Incident 1					
2nd Packet:	nn	8A	d1	d2	d2	d2	d2	cc

			Incident 1	Incident 2				
				:				
				:				
3600th:	nn	0A	dd	dd	dd	dd	dd	cc

			Incident 3000					

7.13 EEL Restart (Test Code 56)

This message is used to initialise the contents of the EEL in the CMM. No data is expected from the CMM in response.

Packet to CMM: nn **01** **03** **56** 00 00 00 cc

7.14 Request S/W Version (Test Code 57)

This message is used to request the Version of the CMM Software.

Packet to CMM: nn **01** **03** **57** 00 00 00 cc

The data is returned in the following format:.

Packet From CMM:	nn	0A	sv	sv	00	00	00	cc
				CMM Minor (BCD)				
			CMM Major (BCD)					

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7.15 Coin Insertion Query (Test Code 58)

This message is used to request count of total coins inserted.

Packet to CMM: nn **01** **03** **58** 00 00 00 cc

The data is returned in the following format:

Packet from CMM: nn **0A** **cc** **cc** 00 00 00 cc

LSB	MSB
Coin Insertions	

7.16 Reset Coin Insertions (Test Code 59)

This message is used to clear the coin insertion counter. No data is expected from the CMM in response.

Packet to CMM: nn **01** **03** **59** 00 00 00 cc

7.17 RAM Dump (Test Code 5A)

This message causes the CMM to send the contents of the RAM in a sequence of packets.

Packet to CMM: nn **01** **03** **5A** 00 00 00 cc

The data is returned in a total of 400 packets comprising 2,000 bytes of data, in the following format.

Packet 1 from CMM: nn **8A** **dd** **dd** **dd** **dd** **dd** cc

RAM Data
⋮
⋮

Packet 400: nn **0A** **dd** **dd** **dd** **dd** **dd** cc

RAM Data

7.18 Stack Clear (Test Code 5B)

This message is used to initialise the contents of the stack in the CMM. No data is expected in response from the CMM.

Packet to CMM: nn **01** **03** **5B** 00 00 00 cc

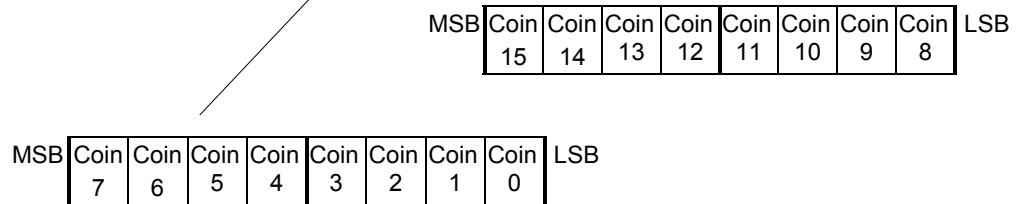
8. Appendix 3: Programmable Data Commands.

8.1 Coin Inhibit Map, (Hex code 01)

This data defines coins to be inhibited in the coin map.(2 bytes long).

1st Packet to CMM: nn 87 01 00 00 00 00 cc

2nd Packet to CMM: nn 07 im im 00 00 00 cc



For each bit Bit = 1, coin is inhibited, Bit = 0, coin is enabled.

8.2 Coin Data Table, (Hex code 02)

1st Packet to CMM: nn 87 02 00 00 00 00 cc

nth Packet to CMM: nn 07 dd dd dd dd cc

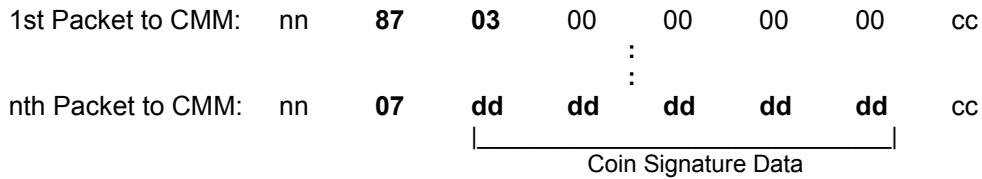
Coin Data

The data, which is 16 bytes per coin in length, is split into blocks, each five bytes long, for transmission in the packet stream. The data structure (per coin) is shown below, therefore for n coins the structure must be repeated n times.

Table 18: Coin Data Table Download Data

Field	Length
Coin Code	1 byte
Coin ID	1 byte
Sig File Id	1 byte
Sig File Id Version	1 byte
Spare	3 bytes
Inhibit Status	1 byte
Coin Value	3 bytes
Coin Displacement	2 bytes
Coin ID Tones	1 byte
Coin Pulses	1 byte
Padding	1 byte
Total	16 bytes

8.3 Coin Signature Table, (Hex code 03)



This data, which is 136 bytes per coin long, is used for coin acceptor remote coin update. The data is split into blocks, each five bytes long, for transmission in the packet stream.

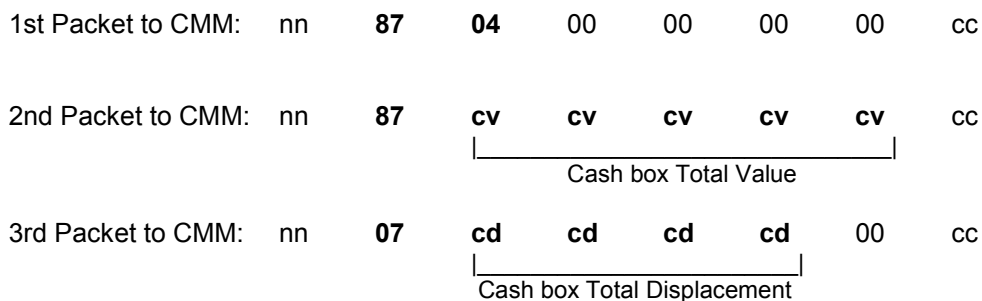
The data structure (per coin) is shown below, therefore for n coins the structure must be repeated n times. The internal data structure varies with the type of acceptor being used, although the data block is always the same size.

Table 19: Coin Signature Table Download Data

Money Controls C120P Download Data	
Field	Length
Coin ID	1 byte
Sig File Id	1 byte
Sig File Id Version	1 byte
Coin Signature:	
Programming Size	1 byte
Coin Type	1 byte
Coin Data	130 bytes
Padding	1 byte
Total	136 bytes

8.4 Cash box Totals, (Hex code 04)

The data, which is 9 bytes long, describes the cash box totals.



8.5 Host Identity, (Hex code 05)

The host ID, which is 6 bytes long, is in packed BCD, each unused nibble filled with Hex E (Decimal 14).

1st Packet to CMM:	nn	87	05	00	00	00	00	cc
2nd Packet to CMM:	nn	87	id	id	id	id	id	cc

			(MSB)	Host ID				
3rd Packet to CMM:	nn	07	id	00	00	00	00	cc
			Host ID (LSB)					

8.6 Coin Count Totals, (Hex code 06)

The data, which is 2 bytes per coin long, describes the current coin counts for each coin.

1st Packet to CMM:	nn	87	06	00	00	00	00	cc
2nd Packet to CMM:	nn	87	dd	dd	dd	dd	00	cc
			LSB	MSB	LSB	MSB		
			Coin 1 Count		Coin 2 Count			
					:			
					:			
1+(n/2)th to CMM:	nn	07	dd	dd	dd	dd	00	cc
			LSB	MSB	LSB	MSB		
			Coin n-1 Count		Coin n Count (00s if n is odd)			

8.7 Report Thresholds, (Hex code 07)

1st Packet to CMM:	nn	87	07	00	00	00	00	cc
2nd Packet to CMM:	nn	87	dd	dd	dd	dd	dd	cc
			Threshold for 1st Item					
7th Packet to CMM:	nn	07	dd	dd	dd	dd	00	cc
					Threshold for 29th Item			

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The data, which is 29 bytes long, describes the value for each fault threshold. The thresholds specify the number of faults of each type which must occur before a fault message is sent across the interface.

A value of 0 disables the fault message for that fault type. The default values of the thresholds are show in the table below.

Table 20: Fault Report Thresholds

No.	Report Type	Default Threshold
1	Motor Current Fail	2
2	Not Used	0
3	Cashing Errors Exceeded	6
4	Not Used	0
5	Refund Errors Exceeded	6
6	Not Used	0
7	Store Blocked	0
8	Not Used	0
9	Store Position Fail	0
10	Not Used	0
11	Store Sensor Fail	2
12	Not Used	0
13	Refund Fail	0
14	Not Used	0
15	Refund Not Cash Fail	0
16	Not Used	0
17	Cash Not Refund Fail	0
18	Not Used	0
19	Flight Deck Status	1
20	Escrow Entry Blockage	2
21	Refund Sensor Blocked	4
22	CMM Memory Fail	2
23	CMM Acceptor Interface Fail	2
24	Acceptor Fraud Attempt	0
25	Not Used	0
26	Insertion Limit Exceeded	1
27	Maximum Rejects Exceeded	0
28	Accept/Reject Ratio Exceeded	0
29	No-Coin Calls Exceeded	0

9. Appendix 4: Additional commands for units fitted with the Scorpion Coin Acceptor

This appendix details the modifications and extension to the CMM Interface Specification to permit the use of Money Controls Scorpion coin acceptors on the Orbit CMM. The main difference with the Scorpion coin acceptor is that signature tables can be up to 5k bytes long rather than the previous 136 bytes for the C120P. This requires the use of an extended message frame format (Up to 251 bytes of data per frame rather than 5) and some additional standard commands. In addition a new format is used within the signature table download to permit multiple coin windows to be programmed with a single signature table.

9.1 Orbit Identification Code (Hex Code 11)

The existing coin Acceptor Configuration Data Message is changed to include an identification byte in packet 2. There are three packets sent by the CMM in response to a Request Acceptor Configuration Data Message (Hex Code 10). These are:

Packet 1:	nn	91	ca	00	00	00	00	cc
Packet 2:	nn	91	vr	vr	vr	xx	00	cc
Packet 3:	nn	11	00	00	00	yy	zz	cc

Where:

- ca = Calibration standard
- vr = Coin Acceptor Reference No
- yy = Signature File ID
- zz = Signature File ID Version

Current xx is set to zero but in future the host system will set xx to 00 for Orbit CMM's fitted with the C120P coin acceptor and to 80 when fitted with the Scorpion coin acceptor.

9.2 CMM Busy due to Signature File Download (Hex Code 17)

This message is sent by the Orbit CMM to signify that a signature file download to the Scorpion coin acceptor is in progress. It is intended to prevent the payphone or host system from switching off the power to the CMM during a critical phase. The command is sent by the CMM at the start and end of the download process and at least once per 20 seconds during the process. This then enables the payphone to instigate a 30 second timeout which should be re-triggered on receipt of the command. While in this mode the power to the CMM should be maintained and no commands issued by the payphone until the process is complete.

Packet from CMM:	nn	17	ss	00	00	00	00	cc
------------------	----	-----------	-----------	----	----	----	----	----

The byte ss will be set to AA for download in process or 00 for download complete. Note that this feature is supplemental to the CMM Parameter Update Status (Hex Code 08) which is issued by the CMM once the signature table has been loaded into the Scorpion coin acceptor. This is simply a means to ensure that power is maintained by the payphone until the programming of the coin acceptor's data is complete.

9.3 Extended Coin Signature Table Download

The normal packet structure for communications between the payphone and CMM can take a maximum of 5 data bytes. While this was acceptable for signature tables of 136 bytes as used by the C120P, the tables used by the Scorpion coin acceptor (up to 5k in length) require a more efficient means of download. The following system switches the standard data packet size over to an extended packet size.

Packet 1 to CMM:	nn	87	08	00	00	00	00	cc
Packet n to CMM:	nn	87	le	data		cc
Final Packet:	nn	07	le	data		cc

Where: data is variable length data defined in section [9.4](#)
 le is the length of that data up to a maximum of 251

In order to simplify the data buffers, the maximum length of data in a packet should be 128 bytes although generically the structure can take up to 251. The data sent by this command to be reconstructed (concatenated) by the CMM software such that it is processed as one complete file.

The successful receipt of the final packet (header of 07 as opposed to 87) will also automatically switch the CMM receiving software back to normal size data packets.

9.4 Scorpion Download Data Structure

The data file sent by the above command (section [9.3](#)) should be constructed as follows:

Header + Signature Table + Header + Signature Table + Termination Header

The file will be contiguous and is not required to be synchronized to the packet size. The header is defined as 6 hexadecimal bytes as follows:

AA 55 bm1 bm2 le1 le2

Where bm is a bit map of the windows to which the following signature table refers. Bm1 is the low order (bits zero thru 7 referring to windows 1 thru 8) and bm2 is the high order (bits zero thru 7 referring to windows 9 thru 16 respectively).

le1 & 2 are the length of the following signature table, low byte first.

The signature table is to be concatenated exactly as supplied by Money Controls.

The Termination header follows similar rules but uses a length of 0xFFFF and signifies to the CMM that the download is complete. The reason for this is so that the bit map bytes can then be used for signature file version tracking information:

AA 55 yy zz FF FF

Where yy is the signature file ID and zz the version number. Note that the only function of this information is so that the CMM can report this data in its Coin Acceptor Configuration Data Message – See section [9.1](#).

There is a special case of the header to delete a previously sent window or to initialise the CMM. This is:

AA 55 bm1 bm2 00 00

The zero length bytes will indicate to the CMM that the windows specified by the bit map should be set to unused. It would thus be good practice to send the following header at the start of a complete new download:

AA 55 FF FF 00 00

Note that this can be followed immediately by the first header and signature table or (if required to be left in an un-programmed state without signature tables) by the termination header.

It should also be noted that this system will set the windows according to the last bit map received. Thus if windows 1,3 & 5 are specified in the first header, along with signature table 1 followed by windows 3, 6 & 7 in the second header and signature table 2, then the result will be that windows 1 & 5 use table 1 and windows 3,6 & 7 use table 2.

10. Appendix 5: Hardware Detailed Requirements.

CMM Pin Connections

1	—	GND
2	—	VCC (+5 ± 0.25V)
3	—	WAKE-UP
4	—	HE-TXD
5	—	HE-CTS
6	—	HE-RXD
7	—	SPARE IO
8	—	VS (2.5 - 5V)
9	—	AGND
10	—	VBAT (5.5 - 6.9V)

Figure 6: CMM Pin Connections

10.1 Description of signal connections

Pin 1	<i>Pin name:</i>	GND
	<i>Status:</i>	Power input
	<i>Signal name:</i>	Digital ground / Digital supply 0V
	<i>Signal:</i>	0Vdc
	<i>Comments:</i>	Vcc (pin 2) ground / 0V connection. Must be commoned externally with AGND (pin 9).
Pin 2	<i>Pin name:</i>	Vcc
	<i>Status:</i>	Power input
	<i>Signal name:</i>	Digital positive dc supply input
	<i>Signal:</i>	Switched +5V0 ±0.25Vdc regulated dc supply. 100mA max, 20mA typ. 3mA idle.
	<i>Comments:</i>	This regulated voltage supplies power to the Orbit controller and coin acceptor hardware.
Pin 3	<i>Pin name:</i>	Wake-up
	<i>Status:</i>	Input
	<i>Signal name:</i>	CMM wake-up input
	<i>Signal:</i>	Open collector, switching to GND. Input has internal 470k pullup resistor.
	<i>Comments:</i>	Active HIGH
Pin 4	<i>Pin name:</i>	HE-TxD
	<i>Status:</i>	Output
	<i>Signal name:</i>	Host Equipment – Transmit Data output
	<i>Signal:</i>	5V0 CMOS

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Pin 5	<i>Pin name:</i> HE-CTS <i>Status:</i> Output <i>Signal name:</i> Host Equipment – Clear To Send output <i>Signal:</i> 5V0 CMOS <i>Comments:</i> Active HIGH. Also referred to as CMM Busy.
Pin 6	<i>Pin name:</i> HE-RxD <i>Status:</i> Input <i>Signal name:</i> Host Equipment – Receive Data input <i>Signal:</i> Open collector, switching to GND. Input has internal 470k pullup resistor.
Pin 7	<i>Pin name:</i> Spare I/O <i>Status:</i> Spare input / output pin for future use. <i>Signal name:</i> Spare I/O <i>Signal:</i> Input has internal 470k pulldown resistor. <i>Comments:</i> For future use. Do not connect any external signal to this pin.
Pin 8	<i>Pin name:</i> Vs <i>Status:</i> Power input <i>Signal name:</i> RAM backup dc supply voltage <i>Signal:</i> +2.5 – 5.0 Vdc <i>Comments:</i> Do not connect any external voltage to this pin. This voltage (2.5-5Vdc) is only required if the Orbit internal SRAM is not battery backed. Note that all Orbit CMM's are supplied with battery backed RAM (internal 3V0 Lithium cell).
Pin 9	<i>Pin name:</i> AGND <i>Status:</i> Power input <i>Signal name:</i> Analogue ground / battery supply 0Vdc <i>Signal:</i> 0Vdc <i>Comments:</i> Vbat (pin 10) ground / 0V connection. Must be commoned externally with GND (pin 1)
Pin 10	<i>Pin name:</i> Vbat <i>Status:</i> Power input <i>Signal name:</i> Analogue positive dc supply input / battery positive dc supply input <i>Signal:</i> +5.5 – 6.9V dc battery supply. 200mA max, 60mA/200ms typ, 0mA idle. <i>Comments:</i> Connection to 6Vdc re-chargeable lead acid battery providing power to the motor, solenoid and opto sensor circuits.

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