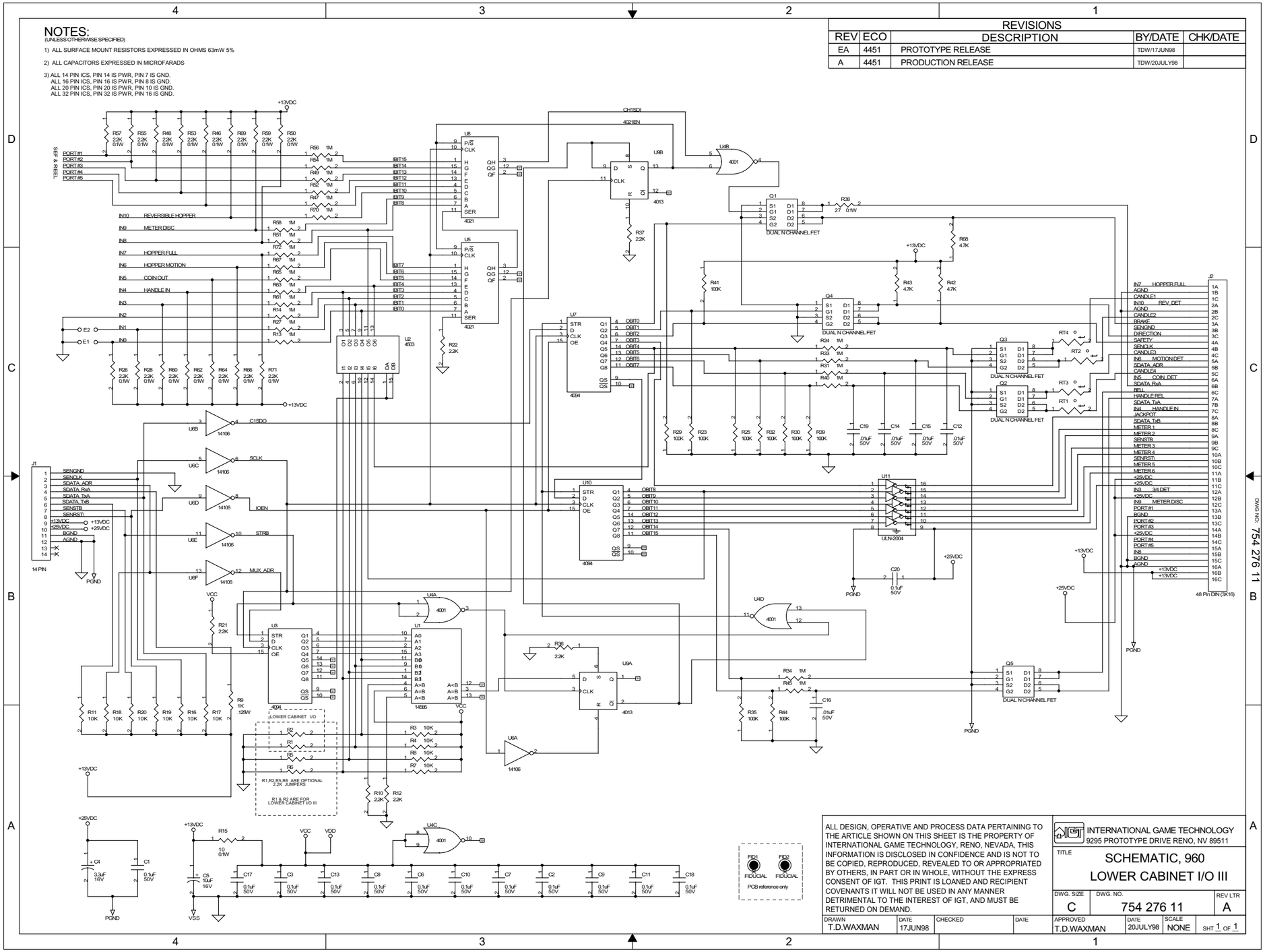
NOTES:
(UNLESS OTHERWISE SPECIFIED)
1) ALL SURFACE MOUNT RESISTORS EXPRESSED IN OHMS 63mW 5%
2) ALL CAPACITORS EXPRESSED IN MICROFARADS
3) ALL 14 PIN ICS, PIN 14 IS PWR, PIN 7 IS GND.
ALL 16 PIN ICS, PIN 16 IS PWR, PIN 8 IS GND.
ALL 20 PIN ICS, PIN 20 IS PWR, PIN 10 IS GND.
ALL 32 PIN ICS, PIN 32 IS PWR, PIN 16 IS GND.
REVISIONS
REV ECO DESCRIPTION BY/DATE CHK/DATE
EA 4451 PROTOTYPE RELEASE TDW/17JUN98
A 4451 PRODUCTION RELEASE TDW/20JULY98
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9295 PROTOTYPE DRIVE RENO, NV 89511
TITLE
SCHEMATIC, 960
LOWER CABINET I/O III
DWG. SIZE C
DWG. NO. 754 276 11
REV LTR A
DRAWN T.D.WAXMAN
DATE 17JUN98
CHECKED
DATE
APPROVED T.D.WAXMAN
DATE 20JULY98
SCALE NONE
SHT 1 OF 1
DWG NO: 754 276 11
PCB reference only