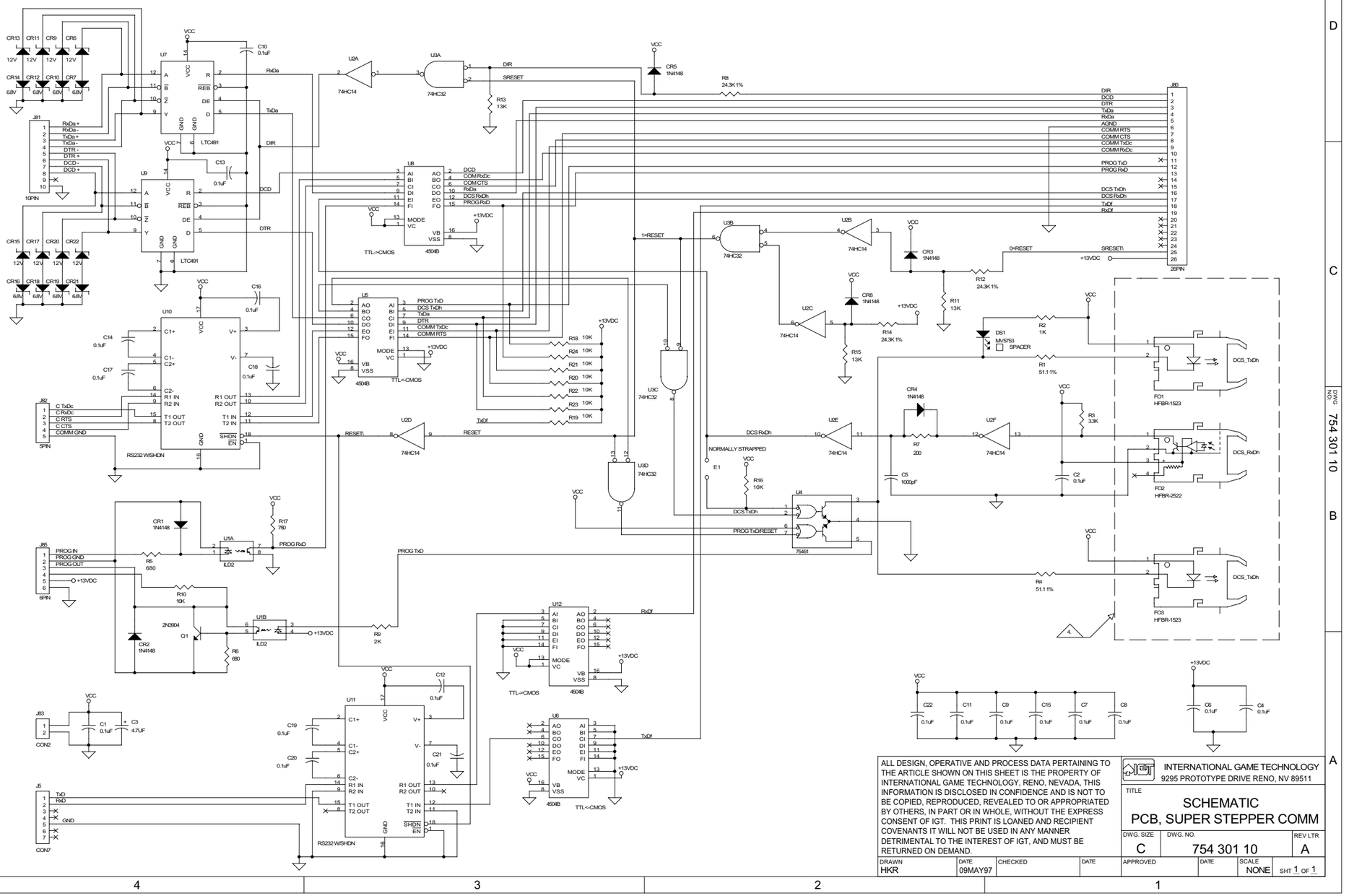
NOTES: (UNLESS OTHERWISE INDICATED)

1. ALL RESISTORS EXPRESSED IN OHM'S 1/4W 5% AND ALL CAPACITORS EXPRESSED IN MICROFARADS
2. ALL 14 PIN IC'S, PIN 14 IS PWR, PIN 7 IS GND. ALL 16 PIN IC'S, PIN 16 IS PWR, PIN 8 IS GND. ALL 20 PIN IC'S, PIN 20 IS PWR, PIN 10 IS GND.
3. STRAP E1 IS REMOVED FOR BOARD TEST. STRAP E2 IS FOR 5V FROM DC POWER SUPPLY (IMPORTANT THAT AC (J84) IS DISCONNECTED).
4. INDICATED PARTS MAY NOT BE USED ON ALL ASSEMBLIES.

REVISIONS				
REV	ECO	DESCRIPTION	BY/DATE	CHK/DATE
EA		PROTOTYPE RELEASE	HKR/09MAY97	
EB		ADD U12	HKR/10JUN97	
A	2675	PRODUCTION RELEASE	HKR/20JUN97	



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INTERNATIONAL GAME TECHNOLOGY
9295 PROTOTYPE DRIVE RENO, NV 89511

TITLE
SCHEMATIC
PCB, SUPER STEPPER COMM

DWG. SIZE	DWG. NO.	REV LTR
C	754 301 10	A

DRAWN	DATE	CHECKED	DATE	APPROVED	DATE	SCALE	SHT 1 OF 1
HKR	09MAY97					NONE	

DWG NO: 754 301 10