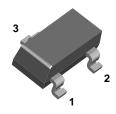
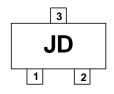
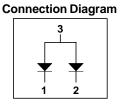


BAW74







SOT-23

Small Signal Diode

Absolute Maximum Ratings*

T₄ = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{RRM}	Maximum Repetitive Reverse Voltage	50	V
I _{F(AV)}	Average Rectified Forward Current	200	mA
I _{FSM}	Non-repetitive Peak Forward Surge Current Pulse Width = 1.0 second Pulse Width = 1.0 microsecond	1.0 2.0	A A
T _{stg}	Storage Temperature Range	-55 to +150	°C
T _J	Operating Junction Temperature	150	°C

^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Thermal Characteristics

Symbol	Parameter	Value	Units
P _D	Power Dissipation	350	mW
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	°C/W

Electrical Characteristics T_A = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Max	Units
V_R	Breakdown Voltage	I _R = 100 μA	50		V
V _F *	Forward Voltage	I _F = 100 mA		1.0	V
I _R *	Reverse Current	V _R = 50 V, T _A = 150°C		100	μΑ
C _T	Total Capacitance	V _R = 0, f = 1.0 MHz		2.0	pF
t _{rr}	Reverse Recovery Time	$I_F = I_R = 10 \text{ mA}, I_{RR} = 1.0 \text{ mA},$ $R_I = 100 \Omega$		4.0	ns

^{*}Pulse test : Pulse width=300us, Duty Cycle=2%

NOTES:

1) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

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PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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